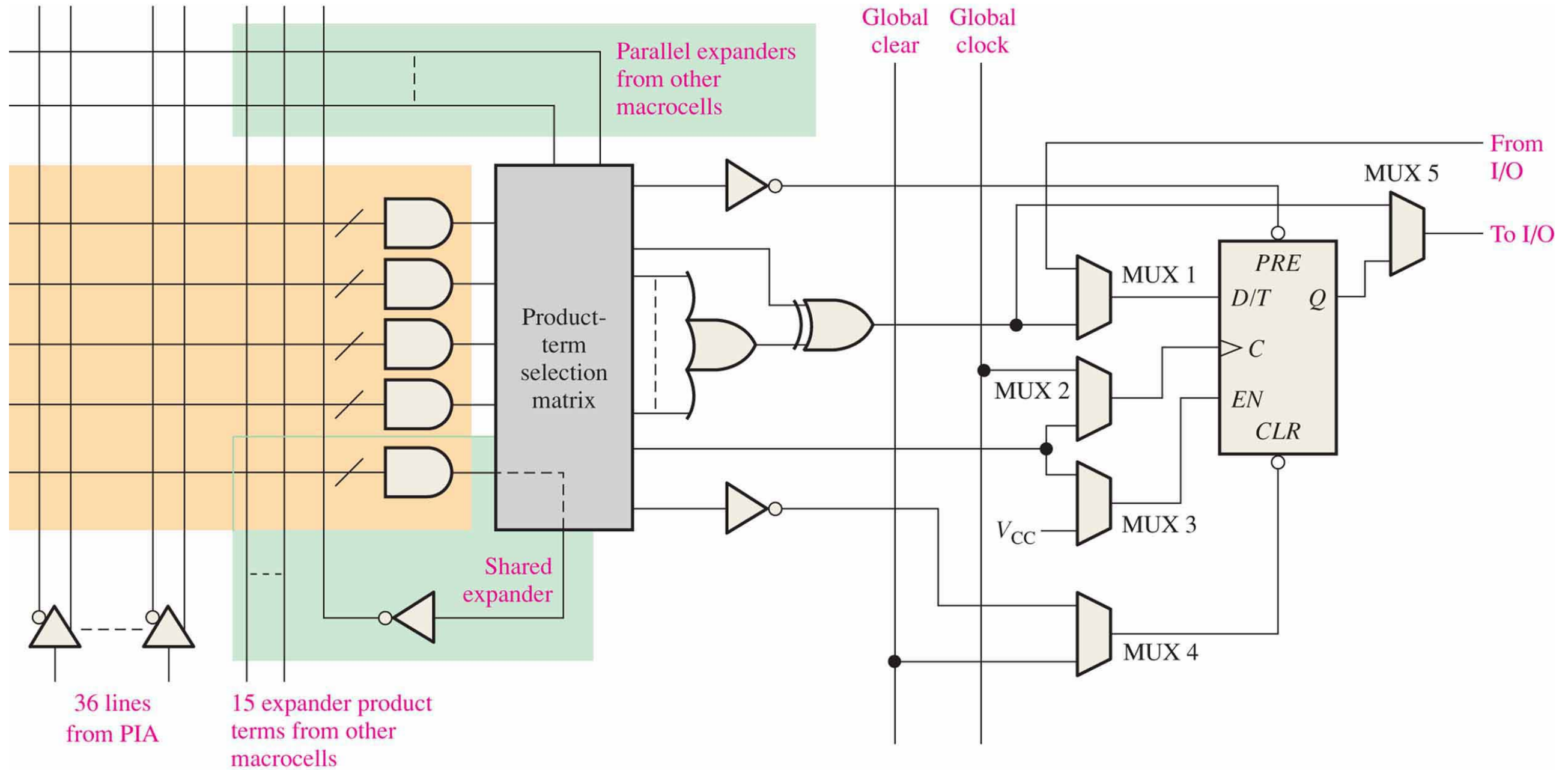
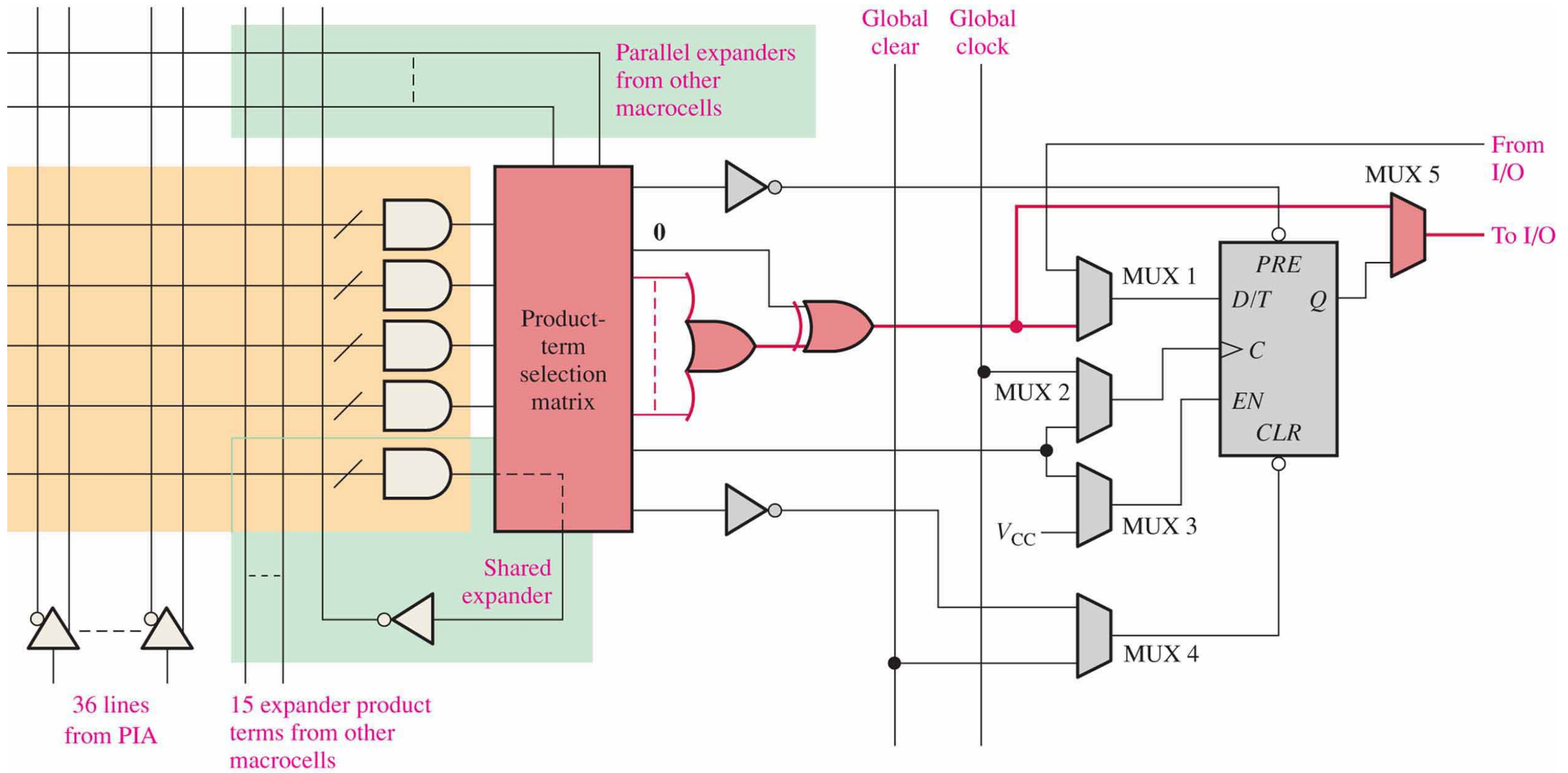


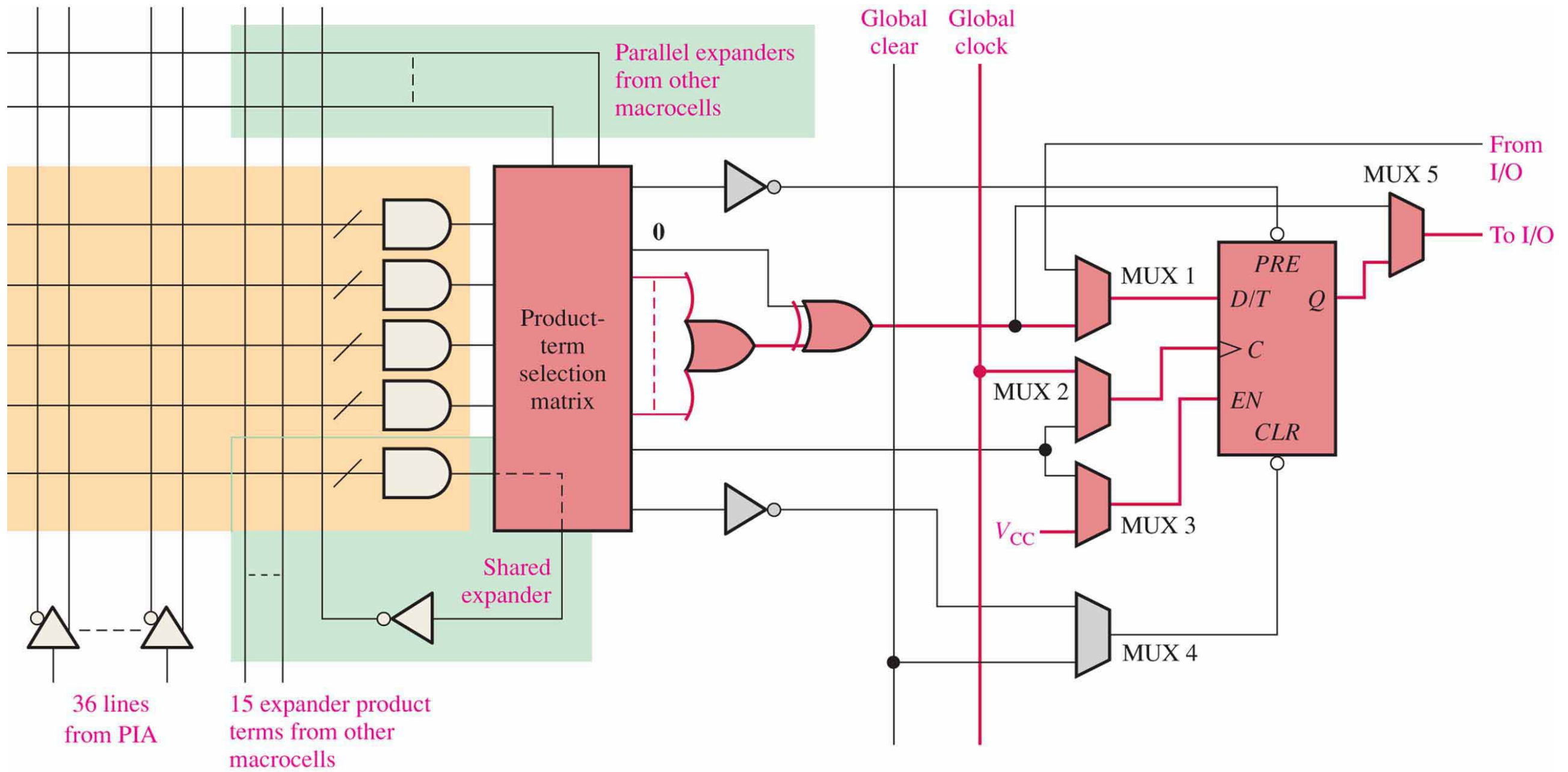
Altera MAX 7000 Macrocells



A macrocell in the Altera MAX 7000 family of CPLDs

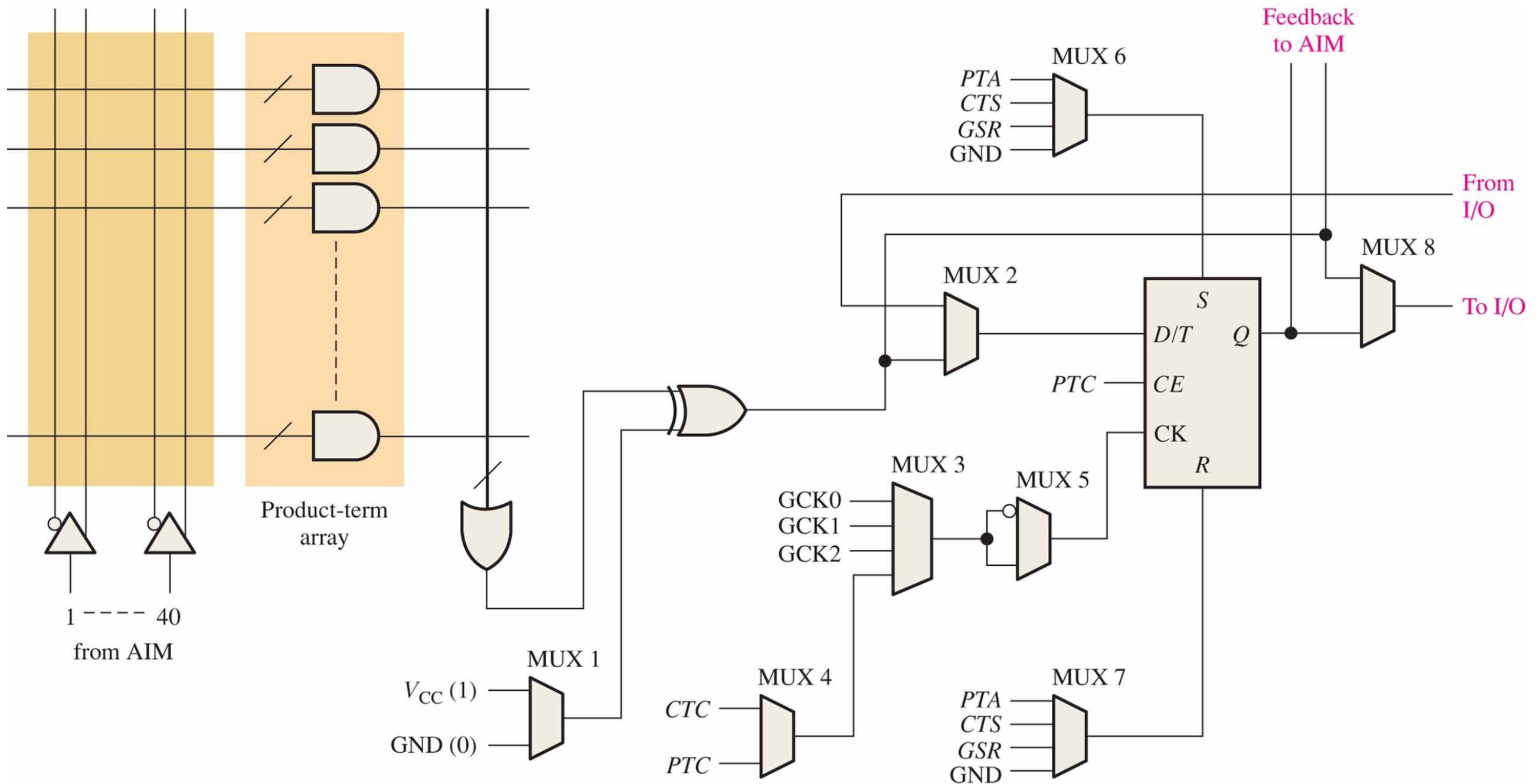


Altera MAX 7000 configured for SOP logic function

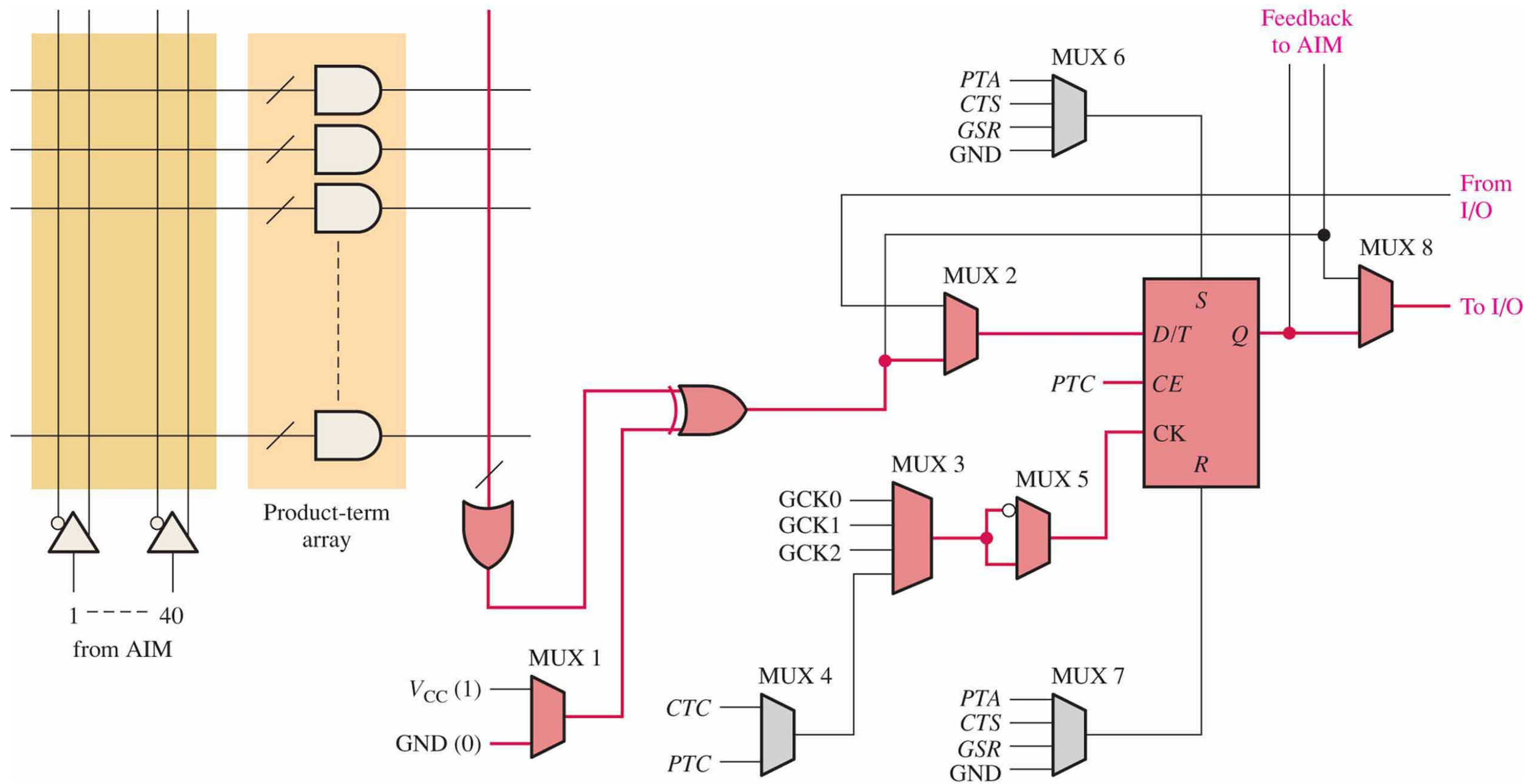


Altera MAX 7000 configured for sequential logic function

Xilinx CoolRunner II Macrocells



A macrocell in the Xilinx CoolRunner II CPLD



Xilinx CoolRunner II configured for registered logic function